COMBINED DECLARATION AND POWER OF ATTORNEY

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name and that I believe I am an original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

TESTING ARCHITECTURE FOR A SEMICONDUCTOR MEMORY DEVICE

the specification of wh	nich					
X is attached here						
was filed on						
as Application S	erial No		and	d was amended on		·
specification, includin I acknowledge tapplication in accorda	g the claims the duty to cance with Tit foreign prio for patent o on for pater priority is cla	, as amendisclose infile 37, Cod rity benefications inventors	ded by an formation e of Fede ts under s certifica	which is material tral Regulations, § Title 35, Untied Stellisted below and	red to above. o the patentab 1.56(a). tates Code, § have also ider	ility of this 119 of any
Number Cour		ıtry	Date Fi	led(yyyy/mm/dd)	Yes	No
						х
Maria Erlinda C. Sarno (Reg. N		t and Trad	lemark Of . 43,330) . 37,436)		ewith: (Reg. No ong (Reg. No	o. 39,081)
SEND CORRESPONDENCE TO:				DIRECT TELEPHONE CALLS TO: (Name and Telephone Number)		
J.C. Patents, Inc. 1340 Reynolds Ave., Suite 114 Irvine, CA 92614 (949) 660-0761				Jiawei Huang (949) 660-0762		

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patents issued thereon.

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